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Jameco Part Number 197835

SPECIFICATIONS

PCI-DIO24

PCI-DIO24H

Digital I/O Boards

Revision 2, March, 2001

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PCI-DIO24

Power consumption

+5V Operating	240 mA typical, 350 mA max

Digital Input / Output

Digital Type	82C55
Configuration	2 banks of 8, 2 banks of 4, programmable by bank as input or output
Number of channels	24 I/O
Output High	3.7 volts min @ −2.5 mA
Output Low	0.4 volts max @ 2.5 mA
Input High	2.2 volts min, 5.3 volts absolute max
Input Low	0.8 volts max, −0.3 volts absolute min
Power-up / reset state	Input mode (high impedance)
Interrupts	INTA# - mapped to IRQn via PCI BIOS at boot-time
Interrupt enable	External (IR ENABLE, active low, disabled by default through internal resistor to TTL high) and programmable through PCI9052; 0 = disabled, 1 = enabled (default)
Interrupt sources	External source (IR INPUT), polarity programmable through PCI9052; 1 = active high, 0 = active low (default)

Environmental

Operating temperature range	0 to 50 °C
Storage temperature range	−20 to 70 °C
Humidity	0 to 90% non-condensing

PCI-DIO24H

Power consumption

+5V Operating	625 mA typical, 960 mA max

Digital Input / Output

Digital Type	8255 mode 0 emulation
Output:	74S244
Input:	74LS373
Configuration	2 banks of 8, 2 banks of 4, programmable by bank as input or output
Number of channels	24 I/O
Output High	2.4 volts min @ −15mA
Output Low	0.5 volts max @ 64 mA
Input High	2.0 volts min, 7 volts absolute max
Input Low	0.8 volts max, −0.5 volts absolute min
Power-up / reset state	Input mode (high impedance)

Interrupts	INTA# - mapped to IRQn via PCI BIOS at boot-time
Interrupt enable	External (IR ENABLE, active low, disabled by default through internal resistor to TTL high) and programmable through PCI9052; 0 = disabled, 1 = enabled (default)
Interrupt sources	External source (IR INPUT), polarity programmable through PCI9052; 1 = active high, 0 = active low (default)

Environmental

Operating temperature range	0 to 50°C
Storage temperature range	–20 to 70°C
Humidity	0 to 90% non-condensing

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